

MAR 15 2004

OFFICIAL

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 09/920,978  
Filing Date ..... August 1, 2001  
Inventor ..... Shane J. Trapp  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Blum, David S.  
Attorney Docket No. .... MI22-1674  
Title: Method of Forming Integrated Circuitry and Method of Forming Shallow  
Trench Isolation in a Semiconductor Substrate

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Dated: March 15, 2004By: Jennifer J. Taylor

Jennifer J. Taylor, Ph.D.

Reg. No. 48,711

Appl. No. 09/920,978

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**RESPONSE TO DECEMBER 15, 2003 OFFICE ACTION**

To: MS Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**AMENDMENTS****Introductory Comments**

In reply to the Office Action of December 15, 2003, applicant amends and remarks  
as follows.